

Fig. 1

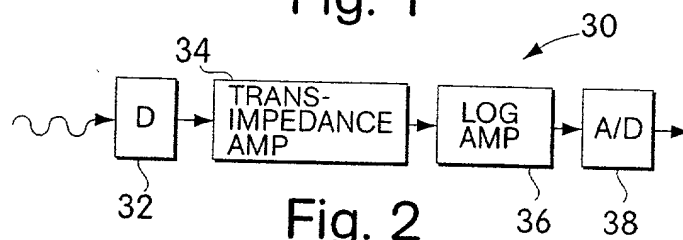


Fig. 2
(PRIOR ART)

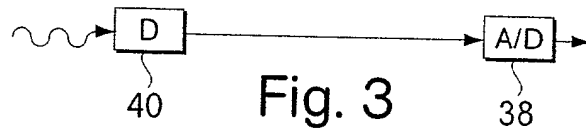


Fig. 3

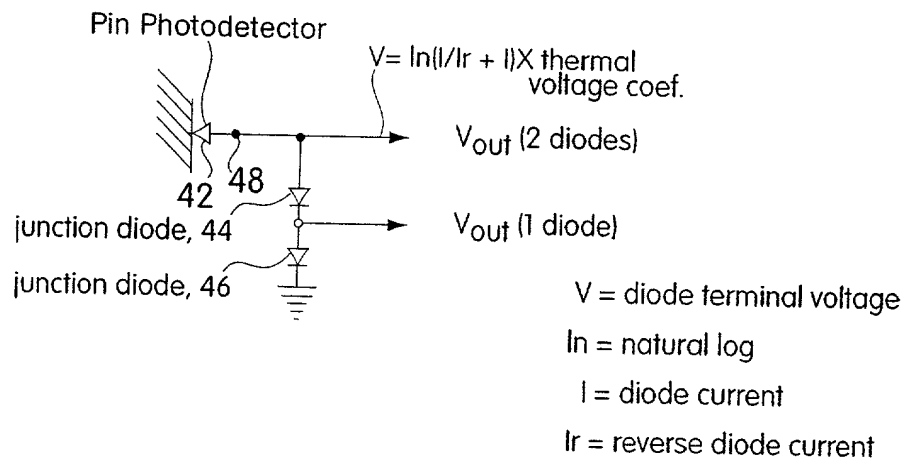


Fig. 4

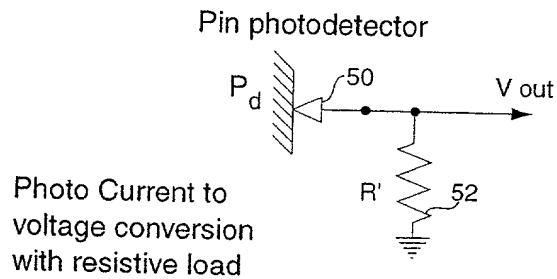


Fig. 5
(PRIOR ART)

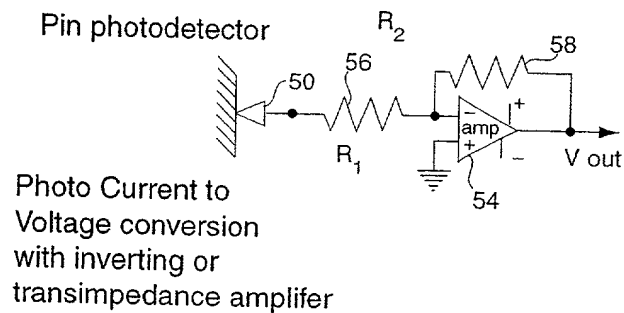


Fig. 6
(PRIOR ART)

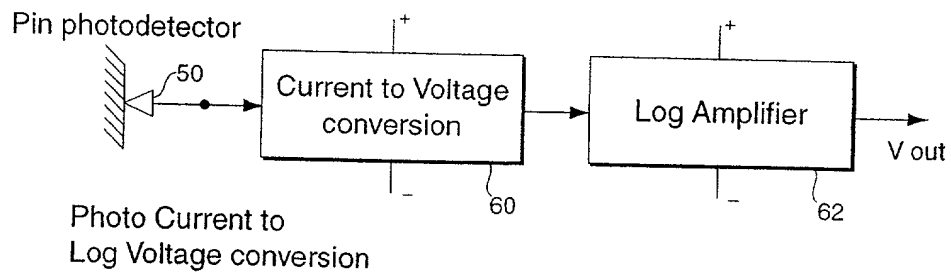


Fig. 7
(PRIOR ART)

OUTPUT VOLTAGE, CONVENTIONAL METHODS

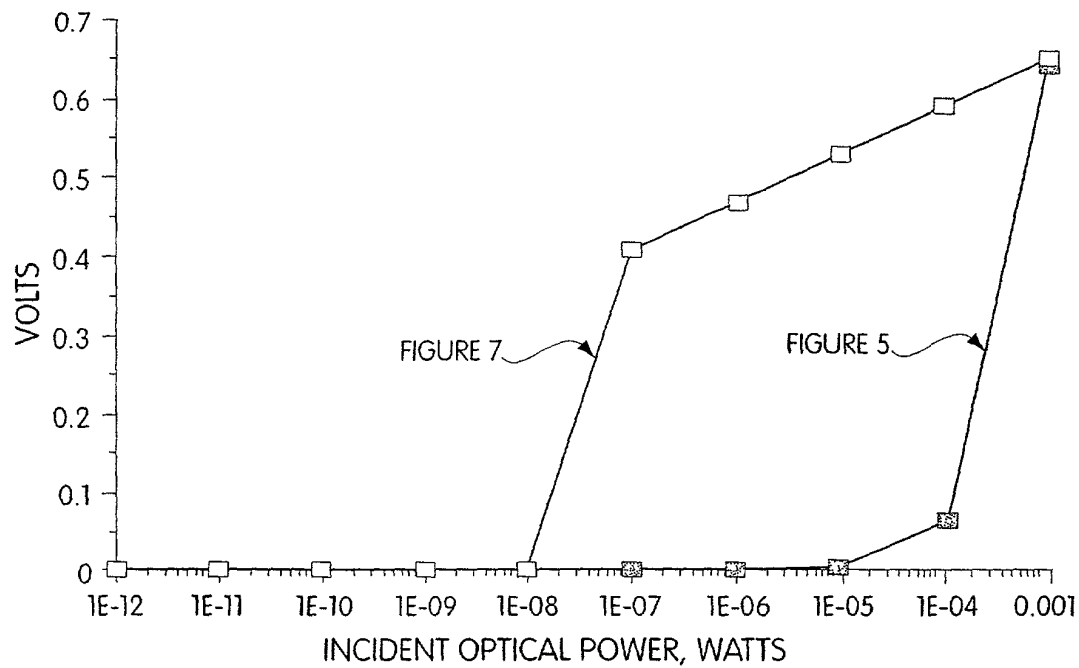


Fig. 8

OUTPUT VOLTAGE, DIODE TERMINATION

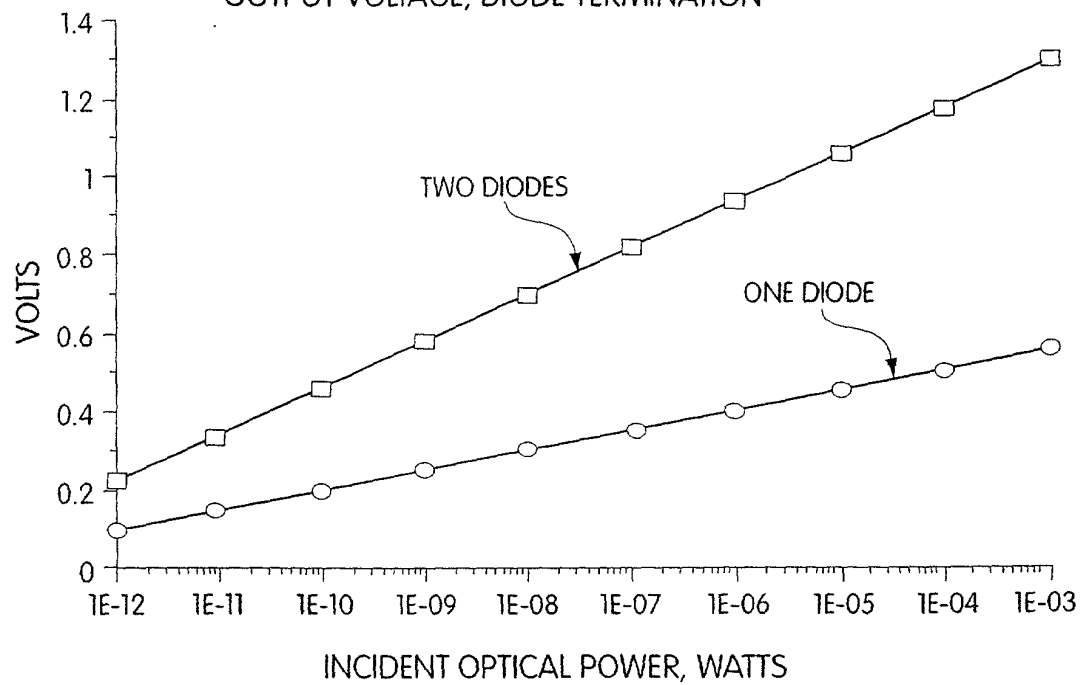


Fig. 9

OUTPUT VOLTAGE VS TEMP, DIODE TERM.

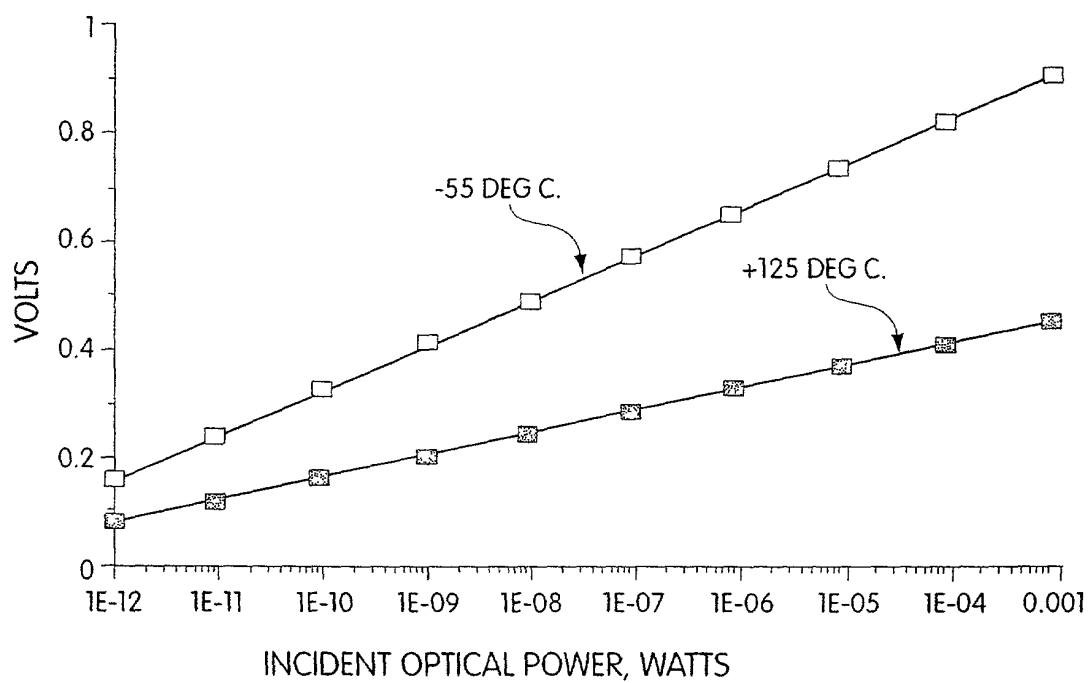


Fig. 10